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**From:** James C. Kesterson **Date:** July 27, 2004  
**Pages:** 4

**Applicant:** Tews, *et al.* **Docket No:** 2004 P 51343 US

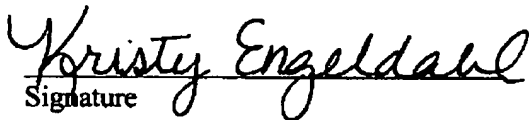
**Serial No.:** 10/840,146 **Art Unit:** 2812

**Filed:** May 6, 2004 **Examiner:** TBD

**For:** Semiconductor Structures and Manufacturing Methods  
**Certificate of Transmission under 37 C.F.R. § 1.8**

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Facsimile Cover Sheet w/Certificate of Transmission (1 page)  
Request for Corrected Filing Receipt (1 page)  
Copy of Filing Receipt Marked to Show Change (2 pages)

  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Tews, *et al.* Docket No.: 2004 P 51343 US  
Serial No.: 10/840,146 Art Unit: 2812  
Filed: May 6, 2004 Examiner: TBD  
Title: Semiconductor Structures and Manufacturing Methods

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**REQUEST FOR CORRECTION**

Sir:

Petitioner hereby requests a correction in the Filing Receipt of the above-referenced patent application.

The first inventor's middle name should be "Horst" not "Horist" as is on the filing receipt. The changes are marked on the filing receipt. Please correct the errors noted and send an updated filing receipt.

No fee is believed due in connection with this filing. However, in the event that there are any fees due, please charge the same, or credit any overpayment, to Deposit Account No. 50-1065.

Respectfully submitted,

\_\_\_\_\_  
July 27, 2004  
Date

\_\_\_\_\_  
*James C. Kesterson*  
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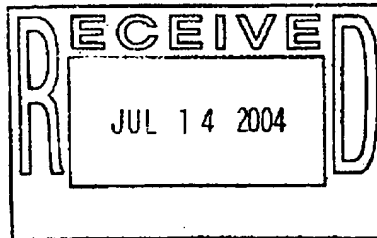
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/840,146	05/06/2004	2812	770	2004 P 51343 US	6	8	3

25962  
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CONFIRMATION NO. 7404

## FILING RECEIPT



\*OC000000013204435\*

Date Mailed: 07/12/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Horst

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## Domestic Priority data as claimed by applicant

This application is a DIV of 09/408,248 09/29/1999 PAT 6,740,555

## Foreign Applications

If Required, Foreign Filing License Granted: 07/10/2004

Projected Publication Date: 10/21/2004

Non-Publication Request: No

Early Publication Request: No

Title

Semiconductor structures and manufacturing methods

Preliminary Class

438

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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